

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
47	BRS	957	fpga same programmable same interconnect	USPAT; US-PGPUB; DERWENT	2003/09/15 08:50		
48	BRS	245	fpga same programmable same interconnect and rows and columns and horizontal and vertical	USPAT; US-PGPUB; DERWENT	2003/09/15 08:50		
49	BRS	149	fpga same programmable same interconnect and rows and columns and horizontal and vertical and interface	USPAT; US-PGPUB; DERWENT	2003/09/15 08:55		
50	BRS	52	fpga same programmable same interconnect and rows and columns and horizontal and vertical and interface and diagonal	USPAT; US-PGPUB; DERWENT	2003/09/16 11:16		
56	BRS	0	freeway same gate same array	USPAT; US-PGPUB; DERWENT	2003/09/15 14:52		
57	BRS	9	freeway same array and architecture	USPAT; US-PGPUB; DERWENT	2003/09/16 11:19		
66	BRS	12	fpga same programmable same interconnect and rows and columns and horizontal and vertical and interface and diagonal and bitstream	USPAT; US-PGPUB; DERWENT	2003/09/16 11:17		
67	BRS	0	freeway same array and architecture and bitstream	USPAT; US-PGPUB; DERWENT	2003/09/16 11:20		
68	BRS	0	freeway same array and architecture and netlist	USPAT; US-PGPUB; DERWENT	2003/09/16 11:20		
69	BRS	0	freeway same netlist and routing	USPAT; US-PGPUB; DERWENT	2003/09/16 11:20		